TOSHIBA Photocoupler PHOTORELAY

TLP3240

Measurement Instruments Logic IC Testers / Memory Testers Board Testers / Scanners

The TOSHIBA TLP3240 is a super small-outline photorelay, suitable for surface-mount assembly. The TLP3240 consists of a GaA ℓ As infrared-emitting diode optically coupled to a photo-MOS FET and housed in a 4-pin package.

Its characteristics also include low OFF-state current and low output pin capacitance, enabling it to be used in high-frequency measuring instruments.

Features

• 4 pin SSOP (SSOP4) : 1.8 mm high, 1.27 mm pitch

• 1-Form-A

Peak off-state voltage : 40 V (Min.)
 Trigger LED current : 3 mA (Max.)
 On-state current : 120 mA (Max.)

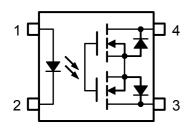
On-state resistance : 14 (Max.), 12 (Typ.)
Output capacitance : 0.8 pF (Max.), 0.45 pF (Typ.)

• Isolation voltage : 1500 Vrms (Min.)

Enlarged drawing is shown on page 4. JEDEC JEITA TOSHIBA 11-2B1

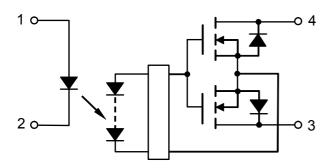
Weight: 0.03 g (Typ.)

Pin configuration (top view)



- 1: Anode
- 2: Cathode
- 3 : Drain
- 4 : Drain

Schematic



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Absolute Maximum Ratings (Ta = 25°C)

	Characteristic	Symbol	Rating	Unit
	Forward current	lF	30	mA
ED	Forward current derating (Ta ≧ 25°C)	ΔI _F /°C	-0.3	mA/°C
۳	Reverse voltage	V _R	5	V
	Junction temperature	Tj	125	°C
	Off-State output terminal voltage	V _{OFF}	40	V
Detector	On-State current	I _{ON}	120	mA
Dete	On-State current derating (Ta ≧ 25°C)	∆l _{ON} /°C	-1.2	mA/°C
	Junction temperature		125	°C
Stora	Storage temperature range		-40~125	°C
Oper	ating temperature range	T _{opr}	-20~85	°C
Lead	soldering temperature (10 s)	T _{sol}	260	°C
Isola	tion voltage (AC, 1 min., R.H. \leq 60%) (Note 1)	BVS	1500	Vrms

(Note 1): Device considered a two-terminal device: Pins 1 and 2 shorted together, and pins 3 and 4 shorted together.

Caution

This device is sensitive to electrostatic discharge. When using this device, please ensure that all tools and equipment are earthed.

Recommended Operating Conditions

Characteristic	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	V_{DD}	_	_	32	V
Forward current	lF	_	_	20	mA
Operating temperature	T _{opr}	25	_	60	°C

Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

Individual Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
	Forward voltage	V _F	I _F = 5 mA	1.15	1.30	1.45	V
LED	Reverse current	I _R	V _R = 5 V	_	_	10	μΑ
	Capacitance	C _T	V = 0, f = 1 MHz		30		pF
Detector	Off-state current	l _{OFF}	V _{OFF} = 35 V		10	200	рА
Dete	Capacitance	C _{OFF}	V = 0, f = 100 MHz, t<1s	_	0.45	0.8	pF

Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Trigger LED current	I _{FT}	I _{ON} = 100 mA	_	_	3	mA
Return LED current	I _{FC}	I _{OFF} = 1 μA	0.1	_	_	mA
On-state resistance	R _{ON}	I _{ON} = 120 mA, I _F = 5 mA, t < 1 s	_	12	14	Ω

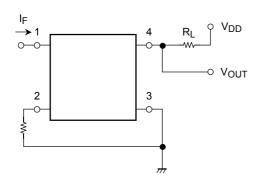
Isolation Characteristics (Ta = 25°C)

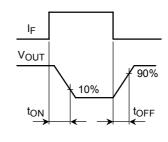
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Capacitance input to output	Cs	V _S = 0 V, f = 1 MHz	_	0.6	_	pF
Isolation resistance	R _S	V _S = 500 V, R.H. ≦ 60%	5 × 10 ¹⁰	10 ¹⁴	_	Ω
		AC, 1 minute	1500	_	_	Vrms
Isolation voltage	BV_S	AC, 1 second (in oil)	_	3000	_	VIIIIS
		DC, 1 minute (in oil)	_	3000	_	Vdc

Switching Characteristics (Ta = 25°C)

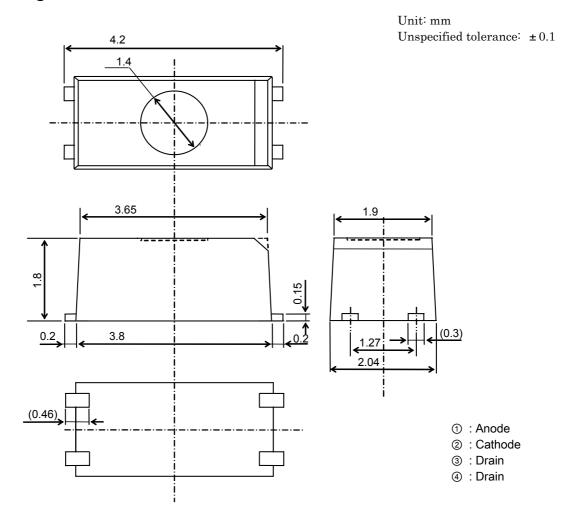
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Turn-on time	t _{ON}	$R_L = 200 \Omega$ (Note 2)	_	26	200	6
Turn-off time	t _{OFF}	$V_{DD} = 10 \text{ V}, I_{F} = 5 \text{ mA}$	_	140	200	μS

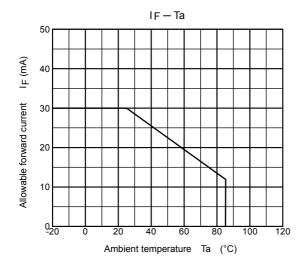
(Note 2): switching time test circuit

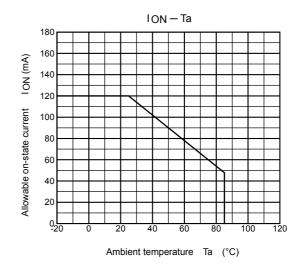


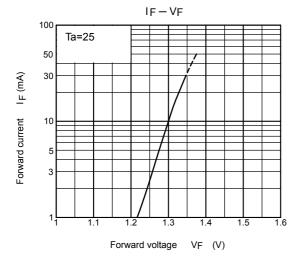


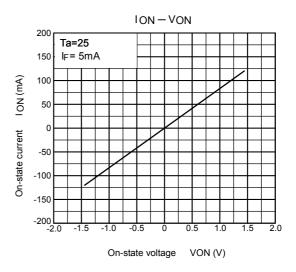
Outline Drawing

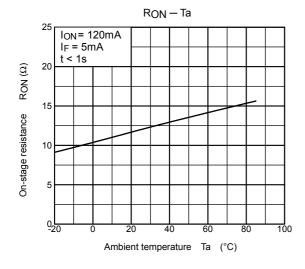


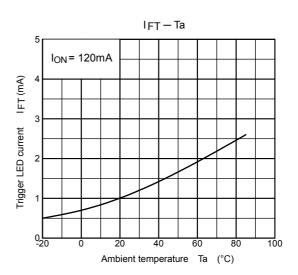


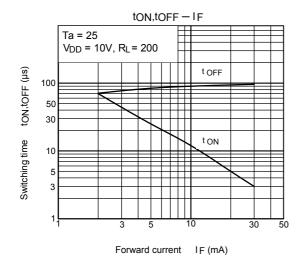


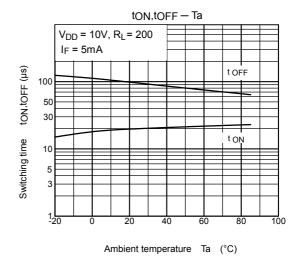


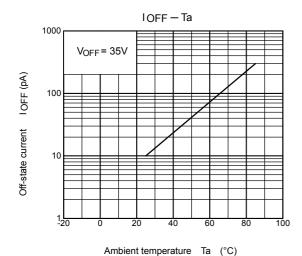












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